

WE CLAIM:

1. A hybrid assembly phase shifter comprising:
a phase delay module comprising a substrate
carrying a plurality of passive, electrically conductive
phase delay elements;
5 a MEMS module containing a plurality of MEMS
switches for coupling selected ones of the phase delay
elements between an input and an output; and
a low loss interconnection electrically
coupling the phase delay elements of the phase delay
10 module with the MEMS switches of the MEMS module.
2. The phase shifter of claim 1 in which:
the low loss interconnection comprises a flip-
chip interconnection.
3. The phase shifter of claim 2 in which:
the flip-chip interconnection comprises an
interconnection selected from the group consisting of
solder bumps, indium bumps, plated-through holes, metal-
5 to-metal thermocompression bonds and conductive polymer
bonds.
4. The phase shifter of claim 1 in which:
the substrate comprises an insulating material.

5. The phase shifter of claim 4 in which:
the substrate comprises a material selected from the group consisting of alumina, quartz and a microwave ceramic.
6. The phase shifter of claim 1 in which:
the substrate comprises a semi-insulating material.
7. The phase shifter of claim 6 in which:
the substrate material comprises a material selected from the group consisting of a high resistivity silicon and GaAs.
8. The phase shifter of claim 1 in which:
each of the plurality of passive phase delay elements comprises electrically conductive, planar transmission lines patterned on a surface of the
5 substrate.

9. A phased array antenna comprising:

a substrate;

a plurality of radiators formed on the substrate;

5 a plurality of passive phase shifter circuits formed on the substrate, each of the plurality of phase shifter circuits being coupled to one of the plurality of radiators and comprising a plurality of phase delay stages connected in series between a transmission signal
10 input and a transmission signal output to phase shift said signal, each of the phase delay stages being capable of imparting a selectable phase delay on a transmission signal so that the signal is delivered to the radiator with a cumulative phase delay determined by the sum of
15 the phase delays imparted by the individual phase delay stages; and

a plurality of MEMS switch modules, one of said MEMS switch modules being coupled to each phase delay stage and operable to electrically connect selected delay
20 stages to provide said cumulative phase delay, the MEMS switch modules being coupled to said phase delay stages by low loss interconnections.

10. The assembly of claim 9 in which:

the low loss interconnections comprise flip-chip interconnections.

11. The assembly of claim 10 in which:
the flip-chip interconnections comprise interconnections selected from the group consisting of solder bumps, indium bumps, plated-through holes, metal-
5 to-metal thermocompression bonds and conductive polymer bonds.
12. The assembly of claim 9 in which:
each of the phase delay stages includes a plurality of phase delay elements comprising true time delay lines of different lengths.
13. The assembly of claim 12 in which:
the true time delay lines comprise electrically conductive, planar transmission lines patterned on a surface of the substrate.
14. The assembly of claim 9 in which:
the substrate comprises an insulating material.
15. The assembly of claim 14 in which:
the substrate comprises a material selected from the group consisting of alumina, quartz and a microwave ceramic.
16. The assembly of claim 9 in which:
the substrate comprises a semi-insulating material.

17. The assembly of claim 16 in which:
the substrate material comprises a material
selected from the group consisting of a high resistivity
silicon and GaAs.